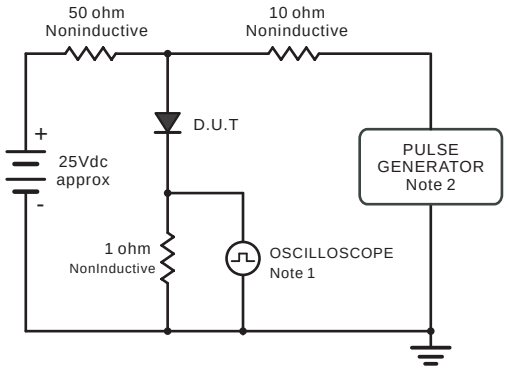
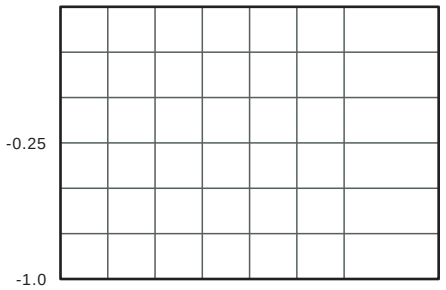
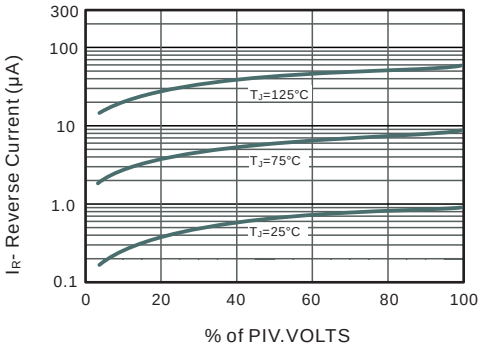
Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram





Note：1. Rise Time = 7ns, max.
Input Impedance = 1megohm,22pF.
2. Ries Time =10ns, max.
Source Impedance = 50 ohms.

Fig.3 Typical Reverse Characteristics



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